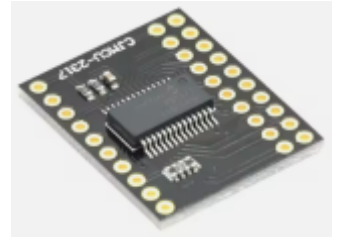


# lamaPLC: MCP23017 / MCP23S17 16-Bit I/O Expander with Serial Interface I<sup>2</sup>C / SPI

The MCP23017/MCP23S17 (**MCP23X17**) (**CJMCU-2317**) device family provides 16-bit, general-purpose parallel I/O expansion for I<sup>2</sup>C bus or SPI applications. The two devices differ only in the serial interface:



- MCP23017 – I<sup>2</sup>C interface
- MCP23S17 – SPI interface

The MCP23X17 contains multiple 8-bit configuration registers for input, output, and polarity selection. The system master can set I/Os as inputs or outputs by writing to the I/O configuration bits (IODIRA/B).

The data for each input or output is stored in the corresponding register. The polarity of the Input Port register can be reversed using the Polarity Inversion register. The system master can read all registers.

The 16-bit I/O port is implemented as two 8-bit ports (PORTA and PORTB). The MCP23X17 can be configured to operate in the 8-bit or 16-bit modes via IOCON.BANK.

## MCP23017 / MCP23S17 Features

Operating voltage: 1.8V to 5.5V

Voltage on Vdd with respect to Vss: **-0.3V to +5.5V**

Communication interface: I<sup>2</sup>C (up to 1.7 MHz), SPI (up to 10 MHz)

Number I/O Pins: 16

Interrupt pins: 2

Operating temperature: -40 °C to 125 °C

Total power dissipation: 700 mW


Maximum current out of Vss pin: 150 mA

Maximum current into Vdd pin: 125 mA

### MCP23017 / MCP23S17 IC Features:

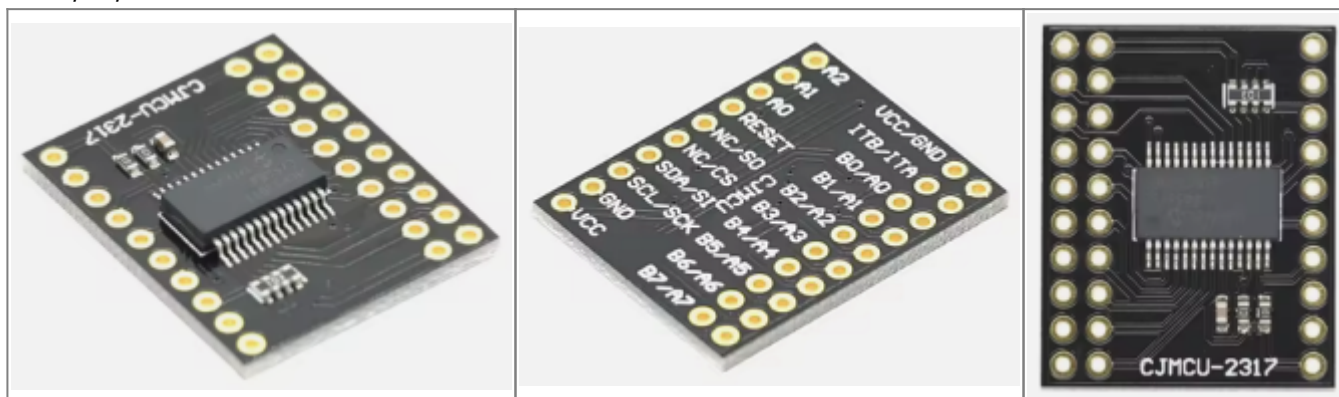
- 16-Bit Remote Bidirectional I/O Port
  - I/O pins default to input
- High-Speed I<sup>2</sup>C Interface (**MCP23017**)
  - 100 kHz
  - 400 kHz
  - 1.7 MHz
- High-Speed SPI Interface (**MCP23S17**)
  - 10 MHz (maximum)
- Three Hardware Address Pins to Allow Up to Eight Devices On the Bus

- Configurable Interrupt Output Pins
  - Configurable as active-high, active-low, or open-drain
- INTA and INTB Can Be Configured to Operate Independently or Together
- Configurable Interrupt Source:
  - Interrupt-on-change from configured register defaults or pin changes
- Polarity Inversion Register to Configure the Polarity of the Input Port Data
- External Reset Input
- Low Standby Current: 1  $\mu$ A (max.)
- Operating Voltage:
  - 1.8V to 5.5V @ -40°C to +85°C
  - 2.7V to 5.5V @ -40°C to +85°C
  - 4.5V to 5.5V @ -40°C to +125°C

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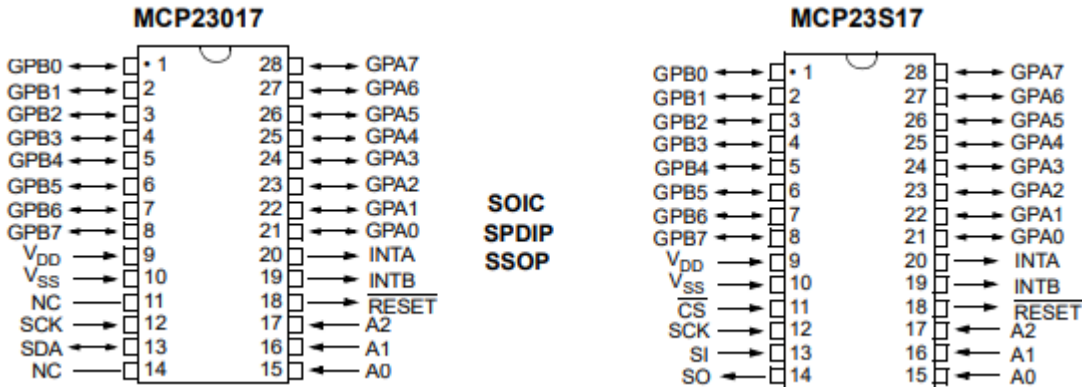


## MCP23017 / MCP23S17 Modul Pinout

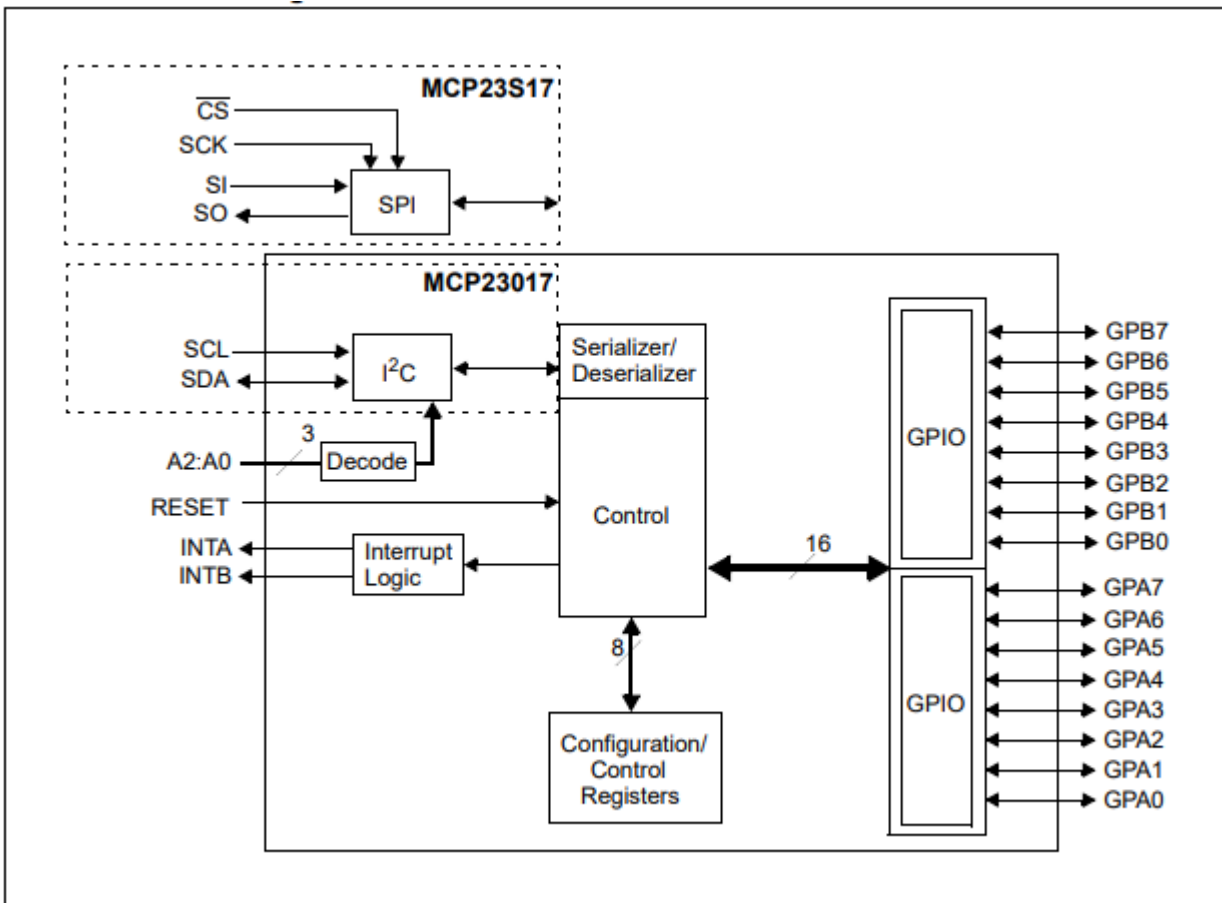
Pin name	Description	Pin name	Description	Pin name	Description
<b>A2</b>	Hardware address pin; look below	<b>Vcc</b>	Power supply (1.8V to 5.5V)	<b>GND</b>	Ground
<b>A1</b>	Hardware address pin; look below	<b>ITB</b>	Interrupt output for PORTB. Can be configured as active-high, active-low, or open-drain	<b>ITA</b>	Interrupt output for PORTA. Can be configured as active-high, active-low, or open-drain
<b>A0</b>	Hardware address pin; look below	<b>B0</b>	digital output pin	<b>A0</b>	digital input pin
<b>RESET</b>	Hardware reset. Must be externally biased	<b>B1</b>	digital output pin	<b>A1</b>	digital input pin
<b>NC/SO</b>	I <sup>2</sup> C nop / SPI slave out	<b>B2</b>	digital output pin	<b>A2</b>	digital input pin
<b>NC/CS</b>	I <sup>2</sup> C nop / SPI chip select	<b>B3</b>	digital output pin	<b>A3</b>	digital input pin

Pin name	Description	Pin name	Description	Pin name	Description
<b>SDA/SI</b>	I <sup>2</sup> C data / SPI slave in	<b>B4</b>	digital output pin	<b>A4</b>	digital input pin
<b>SCL/SCK</b>	I <sup>2</sup> C clock / SPI clock	<b>B5</b>	digital output pin	<b>A5</b>	digital input pin
<b>GND</b>	Ground	<b>B6</b>	digital output pin	<b>A6</b>	digital input pin
<b>Vcc</b>	Power supply (1.8V to 5.5V)	<b>B7</b>	digital output pin	<b>A7</b>	digital input pin

## MCP23017 / MCP23S17 IC pins

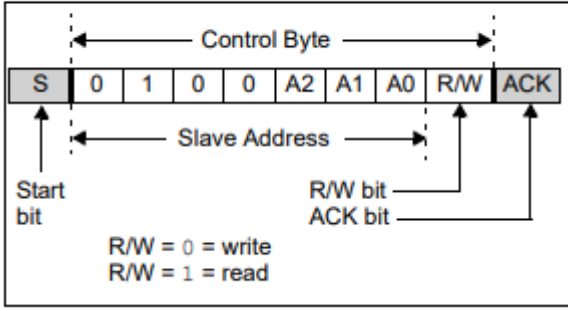
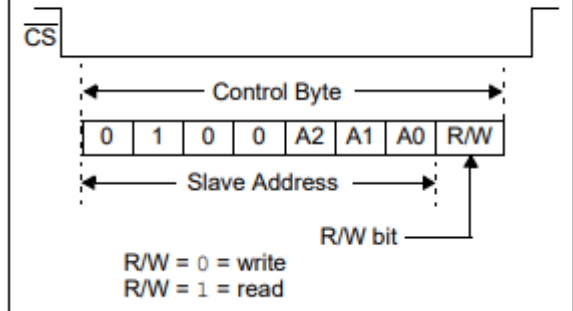


## MCP23017 / MCP23S17 Functional Block Diagram



# MCP23017 / MCP23S17 I<sup>2</sup>C / SPI address coding

The hardware address pins are used to determine the device address. To address a device, the corresponding address bits in the control byte must match the pin state. The pins must be biased externally.

I <sup>2</sup> C CONTROL BYTE FORMAT	SPI CONTROL BYTE FORMAT
 <p>The MCP23017 is a slave I<sup>2</sup>C interface device that supports 7-bit addressing, with the read/write bit filling out the control byte. The slave address includes four fixed bits and three user-defined hardware address bits (pins A2, A1, and A0).</p>	 <p>The MCP23S17 is a slave SPI device. The slave address contains four fixed bits and three user-defined hardware address bits (if enabled via IOCON.HAEN) (pins A2, A1, and A0), with the read/write bit filling out the control byte.</p>

## MCP23017 / MCP23S17 Source

<https://ww1.microchip.com/downloads/en/devicedoc/20001952c.pdf>

### I<sup>2</sup>C topics on lamaPLC

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[bme280, bme680, bme688, bmp180, bmp280, hw-611, hw611, bosch, temperature, humidity, pressure, sensor, arduino, i2c, communication, ai, cjmcu, volatile organic compounds, vocs, volatile sulfur compounds, vscs, iaq](#)
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